| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|------|--|------------------------------|---------------------|---------|------------------|
| S1 | 230 | credit\$\$bas\$3 | US-PGPUB; USPAT | OR | ON | 2004/03/24 18:00 |
| S2 | 177 | credit\$\$bas\$3 and buffer\$3 | US-PGPUB; USPAT | OR | ON | 2006/11/06 16:49 |
| S3 | 18 | credit\$\$bas\$3 and buffer\$3 and (plurality near3 processor) | US-PGPUB; USPAT | OR | ON | 2004/04/07 18:38 |
| S4 | 6 | credit\$\$bas\$3 and buffer\$3 and (inter\$processor) | US-PGPUB; USPAT | OR | ON | 2004/04/07 18:36 |
| S5 | 24 | credit\$\$bas\$3 and (shared adj3 buffer\$3) | US-PGPUB; USPAT | OR | ON | 2004/04/08 12:17 |
| S6 | 7 | credit\$\$bas\$3 and (shared adj3 buffer\$3) and remot\$3 | US-PGPUB; USPAT | OR | ON | 2004/04/08 15:28 |
| S7 | 6 | credit\$\$bas\$3 and buffer\$3 and (plurality near3 processor) and remot\$3 | US-PGPUB; USPAT | OR | ON | 2004/04/07 18:38 |
| S8 | 1 | ("5958019").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2004/04/08 12:17 |
| S9 | 1 | "5634071".PN. | USPAT | OR | OFF | 2004/04/08 12:24 |
| S10 | 1 | "5481747".PN. | USPAT | OR | OFF | 2004/04/08 12:26 |
| S11 | 1 | "5440750".PN. | USPAT | OR | OFF | 2004/04/08 12:26 |
| S12 | 5 | credit\$\$bas\$3 and buffer\$3 and ("I2" near2 cache) | US-PGPUB; USPAT | OR | ON . | 2004/04/08 14:42 |
| S13 | 1 | ("6347337").PN. | US-PGPUB; USPAT; USOCR | OR . | OFF | 2004/04/08 14:42 |
| S14 | 1 | ("6493776").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2004/04/08 15:28 |
| S15 | 1 | "5625846".PN. | USPAT | OR | OFF | 2004/04/08 16:44 |
| S16 | 1 | "5623405".PN. | USPAT | OR | OFF | 2004/04/08 16:45 |
| S17 | 689 | (memory near4 allocat\$3) and credit\$3 and threshold | US-PGPUB; USPAT | OR. | ON | 2005/08/12 09:14 |
| S18 | 90 | (memory near4 allocat\$3) and (return\$3 near4 credit\$3) and threshold | US-PGPUB; USPAT | OR | ON | 2005/08/12 09:14 |
| S19 | 46 | (memory near4 allocat\$3) and (return\$3 near4 credit\$3) and threshold and (memory near4 controller) | US-PGPUB; USPAT | OR | ON | 2005/08/12 09:24 |

| S20 | . 0 | (memory near4 allocat\$3) and (return\$3 near4 credit\$3) and threshold and (memory near4 controller) and (plurality near4 processor) | US-PGPUB; USPAT | OR | ON | 2005/08/12 09:15 |
|-----|-----|---|------------------------------|----|-----|------------------|
| S21 | 24 | (memory near4 allocat\$3) and (return\$3 near4 credit\$3) and (exceed\$3 near4 threshold) and (memory near4 controller) | US-PGPUB; USPAT | OR | ON | 2005/08/12 09:15 |
| S22 | 17 | "5867480" | US-PGPUB; USPAT | OR | ON | 2005/08/12 10:03 |
| S23 | . 4 | "6646985" | US-PGPUB; USPAT | OR | ON | 2005/08/12 10:05 |
| S24 | 30 | (credit\$\$based).ab. | US-PGPUB; USPAT | OR | ON | 2005/08/12 10:06 |
| S25 | 10 | (credit\$\$based).ab. and threshold | US-PGPUB; USPAT | OR | ON | 2005/08/12 10:26 |
| S26 | 1 | ("6078565").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/08/12 10:30 |
| S27 | 3 | "6751698" | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:36 |
| S28 | 54 | (credit\$\$based) and (multi\$\$processor) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:37 |
| S29 | 11 | (credit\$\$based) and (multi\$\$processor) and threshold | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:52 |
| S30 | 39 | (credit\$\$based) and (multi\$\$processor) and cach\$3 | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:42 |
| S31 | 38 | (credit\$\$based) and (multi\$\$processor) and cach\$3 and controller | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:42 |
| S32 | 26 | (credit\$\$based) and (memory near4 controller) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:42 |
| S33 | 0 | (credit\$\$based) and (multi\$\$processor) and (memory near4 controller) and S18 | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:42 |
| S34 | 22 | (credit\$\$based) and (multi\$\$processor) and (memory near4 controller) and cach\$3 | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:45 |

| | | • | | | | |
|-----|------|---|------------------------------|------|------|------------------|
| S35 | 3 | (credit\$\$based) and (multi\$\$processor) and (memory near4 controller) and cach\$3 and router | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:43 |
| S36 | 6 | (credit\$\$based) and (multi\$\$processor) and (memory near4 controller) and cach\$3 and inter\$\$processor | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:44 |
| S37 | 1 | ("6715008").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/08/12 10:44 |
| S38 | 1 | "6715008" | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 10:44 |
| S39 | 54 | (credit\$\$based) and (multi\$\$processor) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 16:24 |
| S40 | 39 | (credit\$\$based) and (multi\$\$processor) and cach\$3 | US-PGPUB; USPAT; USOCR | OR . | ON | 2005/08/12 10:52 |
| S41 | 14 | "6347337" | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 16:29 |
| S42 | 1480 | credit and multi\$\$processor | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 16:29 |
| S43 | 666 | credit and multi\$\$processor and (cach\$3) | US-PGPUB; USPAT; USOCR | OR | ON . | 2005/08/12 16:29 |
| S44 | . 62 | credit and multi\$\$processor and ("I2" near4 cach\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 16:36 |
| S45 | 51 | credit and multi\$\$processor and ("level 2") | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 16:40 |
| S46 | 30 | (credit\$\$based).ab. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 16:53 |
| S47 | 1 | ("6359884").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/08/12 16:55 |
| S48 | 1 | ("5898671").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/08/12 16:56 |
| S49 | 1 | ("6493776").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/07 12:40 |

| S50 | 1 | ("6532501").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/08/12 16:59 |
|-----|------|---|------------------------------|----|-----|------------------|
| S51 | 1 | ("5982771").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2005/08/12 17:00 |
| S52 | 81 | (credit\$\$based) and cach\$3 | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 17:01 |
| S53 | 26 | (credit\$\$based) and cach\$3 and (multiple near4 processor) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 17:03 |
| S54 | 35 | (credit\$\$based) and (multiple near4 processor) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/12 17:03 |
| S55 | 3373 | shah.in. | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 10:48 |
| S56 | 4 | shah.in. and credit\$\$based | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 10:48 |
| S57 | 0 | rejesh.in. and credit\$\$based | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 10:48 |
| S58 | 5 | "6078565" | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 14:58 |
| S59 | 1 | "6715008" | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 15:00 |
| S60 | , 3 | "6751698" | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 16:40 |
| S61 | 0 | (credit\$\$based near4 allocat\$3) and (return\$3 near4 random) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 16:41 |
| S62 | . 0 | (credit\$\$based) and (return\$3 near4 random) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 16:40 |
| S63 | 38 | (credit\$\$based near4 allocat\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2005/08/16 16:41 |
| S64 | 1 | ("6738811").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/01/26 09:38 |

| | | | | , | | |
|-----|------|--|------------------------------|--------------|------|------------------|
| S65 | 1 | ("6892317").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/01/26 15:48 |
| S66 | 1 | ("6347337").PN. | US-PGPUB; USPAT; USOCR | OR . | OFF | 2006/01/26 15:51 |
| S67 | 1 | ("6044406").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/01/27 15:17 |
| S68 | 2 | (("6715008") or ("6751698")).PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/01/27 17:01 |
| S69 | 1955 | (credit near4 based) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:02 |
| S70 | , 66 | (credit near4 based) and (return\$3 near4 random) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:02 |
| S71 | 0 | (credit near4 based) and (return\$3 near4 random) and (plurality near4 source) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:03 |
| S72 | 64 | (credit near4 based) and (return\$3 near4 random) and (plurality) | US-PGPUB; USPAT; USOCR | OR | ON . | 2006/01/27 17:03 |
| S73 | 63 | (credit near4 based) and (return\$3 near4 random) and (source) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:03 |
| S74 | 0 | (credit near4 based) and (return\$3 near4 random near4 source) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:03 |
| S75 | 2 | (credit near4 based) and (return\$3 near4 random near4 credit) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:04 |
| S76 | 45 | (credit near4 based) and (return\$3 near4 random) and (flow\$3 near4 control) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:06 |
| S77 | 376 | (credit\$\$based) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:06 |
| S78 | 123 | (credit\$\$based) and random and (flow\$3 near4 control\$4) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:08 |
| S79 | | (credit\$\$based) and (return\$3 near4 random) and (flow\$3 near4 control\$4) | US-PGPUB; USPAT; USOCR | OR | ON . | 2006/01/27 17:07 |

| S80 | 0 | (credit\$\$based) and (return\$3 near4 random) and (flow\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:07 |
|-----|------|--|------------------------------|------|----|------------------|
| S81 | 0 | (credit\$\$based) and (return\$3 near4 random) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:07 |
| S82 | 24 | (credit\$\$based) and random and (flow\$3 near4 control\$4) and (return\$3 near4 credit) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:11 |
| S83 | 1 | "5825748".PN. | USPAT; USOCR | OR | ON | 2006/01/27 17:10 |
| S84 | 1 | "5737535".PN. | USPAT; USOCR | OR | ON | 2006/01/27 17:10 |
| S85 | 1 | "5734825".PN. | USPAT; USOCR | OR | ON | 2006/01/27 17:10 |
| S86 | 1 | "56338 <u>6</u> 7".PN. | USPAT; USOCR | OR | ON | 2006/01/27 17:10 |
| S87 | 1 | "5515373".PN. | USPAT; USOCR | OR | ON | 2006/01/27 17:11 |
| S88 | 17 | (credit\$\$based).ti. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:11 |
| S89 | 59 | (credit\$\$based or (credit near4 based)).ti. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:21 |
| S90 | 18 | (credit\$\$based or (credit near4 based)).ti. and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:28 |
| S91 | 0 | (credit\$\$based or (credit near4 based)).ti. and (un\$equal\$3 near4 return\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:29 |
| S92 | 0 | (credit\$\$based or (credit near4 based)) and (un\$equal\$3 near4 return\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:29 |
| S93 | 66 | (credit\$\$based or (credit near4 based)) and (random near4 return\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:31 |
| S94 | 63 | (credit\$\$based or (credit near4 based)) and (random near4 return\$3) and sources | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:30 |
| S95 | . 0 | (credit\$\$based) and (random near4 return\$3) | US-PGPUB; USPAT; USOCR | OR - | ON | 2006/01/27 17:30 |
| S96 | 4338 | (credit\$\$based or (credit near4 based)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:31 |

| S97 | 874 | (credit\$\$based or (credit near4 based)) and (flow\$3 near4 | US-PGPUB; USPAT; | OR . | ON | 2006/01/27 17:31 |
|------------------|------------------|--|------------------------------|------|----|------------------|
| | | control\$4) | USOCR | , | | |
| S98 | ['] 440 | (credit\$\$based or (credit near4 based)) and (flow\$3 near4 control\$4) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:32 |
| S99 | 43 | (credit\$\$based or (credit near4 based)).ab. and (flow\$3 near4 control\$4) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:35 |
| S10 0 | 123 | (credit\$\$based) and (flow\$3 near4 control\$4) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:36 |
| S10 1 | 4 | (credit\$\$based) and (flow\$3 near4 control\$4) and random and (plurality near4 source) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:37 |
| S10 2 | 23 | (credit\$\$based) and (flow\$3 near4 control\$4) and random and (multiple near4 source) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:44 |
| S10 3 | 123 | (credit\$\$based) and (flow\$3 near4 control\$4) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:45 |
| S10 4 | 1 | (credit\$\$based) and (flow\$3 near4 control\$4) and (random near4 manner) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:45 |
| S10 5 | 1 | (credit\$\$based) and (random near4 manner) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:46 |
| S10 6 | 2 | (credit\$\$based) and (random near4 credit) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:46 |
| S10 7 | 0 | (credit\$\$based near5 random) | US-PGPUB; USPAT; USOCR | OR . | ON | 2006/01/27 17:47 |
| S10 8 | 38 | (credit\$\$based).clm. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:47 |
| S10 9 | 376 | (credit\$\$based) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:47 |
| S11 0 | 152 | (credit\$\$based) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:48 |
| S11 ¹ | 338 | (credit\$\$based) and (random generat\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 17:49 |

| | | | | | | 2005/04/07 40 07 |
|----------|----------|--|------------------------------|-----|-----------------|------------------|
| S11 2 | 16 | (credit\$\$based) and (random near4 generat\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 18:07 |
| S11 3 | 40 | (credit\$\$based near4 allocat\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/27 18:13 |
| S11 4 | 109 | (credit near4 based near4 allocat\$3) | US-PGPUB; USPAT; USOCR | OR. | ON | 2006/01/27 18:13 |
| S11 5 | 19 | (credit near4 based near4 allocat\$3) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/28 12:40 |
| S11 6 | 0 | (joe near4 avalino).xa. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/28 12:41 |
| S11 7 | 50 | (joseph near4 avellino).xa. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/28 12:46 |
| S11 8 | 33 | (dustin near4 nguyen).xa. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/01/28 13:32 |
| S11 9 | 2 | (("6426945") or ("6317438")).PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/01/28 13:32 |
| S12 0 | 1 | ("6754739").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/07 08:44 |
| S12 · | 4 | (("6347337") or ("6044406") or ("6715008") or ("6751698")).PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/07 08:45 |
| S12 2 | 1510 | "I4" and (over\$\$flow) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:46 |
| S12 3 | 3144 | "I2" and (over\$\$flow) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:46 |
| S12 4 | . 3 | S121 and (over\$\$flow) | US-PGPUB; USPAT; USOCR | OR | ON _. | 2006/06/07 08:47 |
| S12 5 | 2 | S121 and (random) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:48 |
| S12 6 | . 0 | (credit\$\$based near10 (random)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:48 |

| S12 7 | 423 | (credit\$\$based) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:48 |
|----------|-----|--|---|----|----|------------------|
| S12 8 | 0 | (credit\$\$based) and (random near4 return\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:49 |
| S12 9 | 1 | (credit\$\$based) and (random near4 manner) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:51 |
| S13 0 | 48 | (shar\$3 near4 buffer near5 pool) and credit | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:51 |
| S13 1 | 28 | (shar\$3 near4 buffer near5 pool) and credit and threshold | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 08:52 |
| S13 2 | 28 | (shar\$3 near4 buffer near5 pool) and credit and threshold | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:04 |
| S13 3 | 14 | (shar\$3 near4 buffer near5 pool) and credit and cach\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:08 |
| S13 4 | 12 | (shar\$3 near4 buffer near5 pool) and credit and round\$\$trip | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:20 |
| S13 5 | 20 | (shar\$3 near4 buffer near5 pool) and credit and random | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:28 |
| S13 6 | 268 | (shar\$3 near4 buffer) and credit and random | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:29 |
| S13 7 | 29 | (shar\$3 near4 buffer) and credit and (random near5 return\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:28 |
| S13 8 | 2 | (shar\$3 near4 pool) and credit and (random near5 return\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:29 |
| S13 9 | 34 | (shar\$3 near4 buffer) and credit\$\$based and random | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:37 |

| S14 0 | 79 | (shar\$3 near4 buffer) and (credit near4 based) and random | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:38 |
|----------|------|--|---|----|----|------------------|
| S14 1 | 29 | (shar\$3 near4 buffer) and (credit near4 based) and (random near4 return\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:38 |
| S14 2 | 30 | (shar\$3 near4 buffer) and (credit near4 based) and random and skew\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:39 |
| S14 3 | . 37 | (shar\$3 near4 buffer) and (credit near4 based) and skew\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:41 |
| S14 4 | . 12 | (shar\$3 near4 buffer) and (credit near4 based) and (round near4 robin near5 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:44 |
| S14 5 | 0 | (credit near4 based) same (round near4 robin near5 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:44 |
| S14 6 | 0 | (credit near4 based) same (round same robin same random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:44 |
| S14 7 | 0 | (credit near4 based) same (round same trip same random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:44 |
| S14 8 | 24 | (credit near4 based) same (round same trip) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:44 |
| S14 9 | 24 | (credit near4 based) same (round near5 trip) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:47 |
| S15 0 | 0 | (credit near4 based) same (random near4 return\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:47 |
| S15 1 | 72 | (credit near4 based) same (random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:52 |

| S15 2 | 1 | (credit near4 based) same (random near5 pay) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:49 |
|----------|------|--|---|-----|-----|------------------|
| S15 3 | 14 | S151 and threshold | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:48 |
| S15 4 | 1 | (credit near4 based) same (random near5 manner) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:49 |
| S15 5 | . 16 | (credit near4 based) same (shar\$3 near4 buffer\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 09:57 |
| S15 6 | 67 | (credit same (shar\$3 near4 buffer\$3)) | US-PGPUB; USPAT; USOCR; EPO; JPO | ·OR | ON | 2006/06/07 09:57 |
| S15 7 | 9 | (credit same (shar\$3 near4 buffer\$3) same return\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 10:51 |
| S15 8 | 36 | (credit same (shar\$3 near4 buffer\$3)) and (credit\$3 near4 return\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 10:54 |
| S15 9 | 106 | (garage near4 opener) and chamberlain.as. | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 10:54 |
| S16 0 | | (garage near4 opener) and chamberlain.as. and adjustment | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 10:54 |
| S16 1 | . 3 | (garage near4 opener) and chamberlain.as. and (adjustment near4 mechanism) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 10:57 |
| S16 2 | | (garage near4 opener) and chamberlain.as. and (adjustment near4 travel\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 10:57 |
| S16 3 | 1 | ("6347337").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/07 14:48 |

| S16 4 | 8 | "6115748" | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 12:50 |
|----------|---|----------------------|------------------------------|----|----|------------------|
| S16 5 | 4 | "6115748" and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 12:48 |

11/12/06 3:22:31 PM C:\Documents and Settings\DNguyen18\My Documents\EAST\Workspaces\09651924.wsp

| S16 | 248 | ("3804991" "3974343" "4069399" | US-PGPUB; | OR | ON | 2006/06/07 12:50 |
|---------|--------------|---|----------------|--|----|------------------|
| 6 | | "4603382" "4715030" | USPAT; | | | |
| | | "4727537" "4737953" "4797881" | USOCR | | | |
| | | "4821034" "4837761" | | | | |
| | | "4845710" "4849968" "4870641" | | | | |
| | | "4872159" "4872160" | | | | |
| | | "4878216" "4893302" "4893307" | | | | • |
| | | "4894824" "4897841" | | | | · . |
| | | "4899333" "4920531" "4922503" | | | | |
| | | "4933938" "4947390" | | | | |
| | | "4953157" "4956839" "4958341" | | | | |
| | | "4979100" "4993018" | | | | |
| | | "5021949" "5029164" "5060228" | | | | |
| | | "5067123" "5070498" | | The Control of the Co | | • |
| | | "5083269" "5084867" "5084871" | | | | |
| | | "5090011" "5090024" | | | } | · |
| | | "5093912" "5115429" "5119369" | | | | |
| | | "5119372" "5128932" | | | | |
| | | "5130975" "5130982" "5132966" | | | | |
| | | "5146474" "5146560" | | | 1 | |
| | | "5150358" "5151897" "5157657" | | | | |
| | | "5163045" "5163046" | | | | |
| | | "5179556" "5179558" "5185743" | | | | |
| | | "5191582" "5191652" | | | | |
| | | "5193151" "5197067" "5198808" | | | | |
| | | "5199027" "5239539" | | | | |
| | | "5253247" "5253248" "5255264" | | | | |
| | | "5255266" "5257311" | | | | |
| | | "5258979" "5265088" "5267232" | | | | |
| | | "5268897" "5271010" | | | | |
| | | "5272697" "5274641" "5274768" | | | | |
| | | "5278828" "5280469" | | ŀ | | |
| | | "5280470" "5282201" "5283788" | | | | |
| | | "5285446" "5287349" | | | | |
| ! | | "5287535" "5289462" "5289463" | | | | |
| | | "5289470" "5291481" | | | • | |
| - | | "5291482" "5295134" "5301055" | | | | |
| | | "5301184" "5301190" | | | | |
| | | "5301193" "5303232" "5305311" | | | | |
| | | "5309431" "5309438" | | | | |
| | | "5311586" "5313454" "5313458" | | | | |
| | | "5315586" "5319638" | | | | 7 |
| | | "5321695" "5323389" "5333131" | | | | |
| | | "5333134" "5335222" | | | | |
| | | "5335325" "5339310" "5339317" | | | | |
| 1 | | "5339318" "5341366" | | | | |
| | | "5341373" "5341376" "5345229" | | | | |
| • | | "5350906" "5355372" | | | | |
| | | "5357506" "5357507" "5357508" | | | | |
| | | "5357510" "5359592").PN. OR | | | | |
| | | ("5359600" "5361251" "5361372" | | | | |
| | | "5363433" "5365514" | | | | |
| | | "5371893" "5373504" "5375117" | | | | |
| | | | | | | |
| | | | | | | |
| | | "5379297" "5379418" "5390170" "5390174" "5390175" | | | | |
| 1/12/06 | 3:22:31 PM | | | | | Page 1 |
| | nents and Se | | rkspaces\09651 | 924.wsp | | |
| 1 | | | I | 1 | | 1 |

| S16 | 248 | ("3804991" "3974343" "4069399" | US-PGPUB; | OR | ON | 2006/06/07 12:51 |
|---------|--------------|--|---------------------|----------------|----|------------------|
| 7 . | | "4603382" "4715030" | USPAT; | | • | |
| | | "4727537" "4737953" "4797881" | USOCR | | | |
| | | "4821034" "4837761" | | - | | |
| | | "4845710" "4849968" "4870641" | | | | |
| | | "4872159" "4872160" | į | | | • |
| | | "4878216" "4893302" "4893307" | | | | · |
| | | "4894824" "4897841" | | | | |
| | | "4899333" "4920531" "4922503" | - | | | |
| | | "4933938" "4947390" | | | | • |
| | - | "4953157" "4956839" "4958341" | | | | |
| | | "4979100" "4993018" | | | | |
| | | "5021949" "5029164" "5060228" | | | | |
| | | "5067123" "5070498" | | | | |
| | | "5083269" "5084867" "5084871" | | | | |
| | | "5090011" "5090024" | | | | |
| | | "5093912" "5115429" "5119369" | | | | |
| | | "5119372" "5128932" | | | | |
| | | "5130975" "5130982" "5132966" | | | | |
| | - | "5146474" "5146560" | | | | |
| | | "5150358" "5151897" "5157657" | , | | | |
| | | "5163045" "5163046" | | | | |
| | | "5179556" "5179558" "5185743" | | | | |
| | | "5191582" "5191652" | | | | |
| | | "5193151" "5197067" "5198808" | | | | |
| | | "5199027" "5239539" | | | | |
| | | "5253247" "5253248" "5255264" | | | | |
| , | | "5255266" "5257311" | | | , | |
| | | "5258979" "5265088" "5267232" | | - | | - |
| | | "5268897" "5271010" | | | | |
| | | "5272697" "5274641" "5274768" | | | | |
| | | "5278828" "5280469" | | | | |
| | | "5280470" "5282201" "5283788" | | | | |
| | | | | | | |
| | | "5287535" "5289462" "5289463" | | | | , |
| | | | | | | |
| | | "5291482" "5295134" "5301055" | | | : | |
| | | | · | | | |
| | | "5301193" "5303232" "5305311" | · | | | |
| | | "5309431" "5309438" | | | | |
| | | "5311586" "5313454" "5313458" | | | | |
| | | "5315586" "5319638" | | | | |
| | | "5321695" "5323389" "5333131" | | | | |
| | | "5333134" "5335222" | | | | |
| | | "5335325" "5339310" "5339317" | | | | |
| | | "5339318" "5341366" | | | | |
| | | "5341373" "5341376" "5345229" | 1 | | | |
| | | | | | | |
| | | "5357506" "5357507" "5357508" | | | | |
| - | | "5357510" "5359592").PN. OR | | | | |
| | | ("5359600" "5361251" "5361372" | · | | | • |
| | : | "5363433" "5365514" | | | | |
| | | "5371893" "5373504" "5375117" | | | | |
| | | "5377262" "5377327" | | | | |
| | | "5379297" "5379418" "5390170" | | | | |
| 11/12/0 | 6 3:22:31 PM | "5390174" "5390175" | | | | Page 14 |
| | ments and Se | "5392280" "5392402" "5394396" ttings\Divouven18\My Documents\EAST\Wo | rkspaces\09651 | 924.wsp | | , age 1 |
| 1 1-0ca | | TT -5394397" 17'5398235" 1" CT | | ····- - | | |

| S16 8 | 3 | S166 and random and credit | US-PGPUB; USPAT; | OR | ON | 2006/06/07 12:52 |
|----------|-----|---|---|------|-----|------------------|
| S16 9 | 60 | (shar\$3 near4 buffer\$3) and (return\$3 near4 credit) | USOCR US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 12:53 |
| S17 0 | 46 | (shar\$3 near4 buffer\$3) and (return\$3 near4 credit) and random | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 12:57 |
| S17 1 | 0 | (return\$3 same credit same random same shar\$3 same buffer\$3) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/07 12:58 |
| S17 2 | 0 | (return\$3 same credit same random same shar\$3 same buffer\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 12:58 |
| S17 3 | 4 | (return\$3 same credit same random same buffer\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR . | ON | 2006/06/07 12:59 |
| S17 4 | 1 | (credit) and (shar\$3 near4 buffer\$3) and (assign\$3 near4 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 12:59 |
| S17 5 | 16 | (credit near5 assign\$3 near5 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 13:00 |
| S17 6 | 1 | (credit near5 buffer\$3 near5 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 13:00 |
| S17 7 | 2 | (credit near5 pool near5 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 13:00 |
| S17 8 | . 0 | (credit near5 shar\$3 near5 random) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/06/07 13:08 |
| S17 9 | 1 | ("6515963").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/07 13:08 |
| S18 0 | 2 | (("6,715,008") or ("6,751,698")).PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/07 14:48 |

11/12/06 3:22:31 PM C:\Documents and Settings\DNguyen18\My Documents\EAST\Workspaces\09651924.wsp

| S18 1 | 1 | ("6876625").PN. | US-PGPUB; USPAT; USOCR | OR · | OFF | 2006/06/07 18:07 |
|----------|------|---|---|------|------|------------------|
| S18 2 | 1210 | ((credit\$3 near4 based) or credit\$\$bas\$3) and buffer\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/06 16:50 |
| S18 3 | 133 | ((credit\$3 near4 based) or credit\$\$bas\$3) and buffer\$3 and threshold and robin | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/06 16:50 |
| S18 4 | 113 | ((credit\$3 near4 based) or credit\$\$bas\$3) and buffer\$3 and threshold and robin and controller | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/06 16:51 |
| S18 5 | 54 | ((credit\$3 near4 based) or credit\$\$bas\$3) and buffer\$3 and threshold and robin and controller and (return\$3 near4 credit\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR . | ON | 2006/11/06 16:54 |
| S18 6 | 35 | ((credit\$3 near4 based) or credit\$\$bas\$3) and buffer\$3 and threshold and robin and controller and ((pay or paid) near4 credit\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/06 16:55 |
| S18 7 | 42 | buffer\$3 and threshold and robin and controller and ((pay or paid) near4 credit\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON · | 2006/11/06 16:56 |
| S18 8 | 26 | buffer\$3 and threshold and robin and controller and (credit\$3).clm. | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/06 17:03 |
| S18 9 | 45 | buffer\$3 and threshold and robin and controller and (credit\$3 near4 allocat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR 1 | ON | 2006/11/06 17:14 |
| S19 0 | 56 | buffer\$3 and threshold and robin and (credit\$3 near4 allocat\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/07 09:55 |
| S19 1 | . 1 | ("6102969").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/11/07 09:55 |
| S19 2 | 0 | ("(credit\$\$based)and(buffer\$3near4 allocat\$3)").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | OFF | 2006/11/12 14:00 |

11/12/06 3:22:31 PM C:\Documents and Settings\DNguyen18\My Documents\EAST\Workspaces\09651924.wsp Page 16

| S19 3 | 70 | (credit\$\$based) and (buffer\$3 near4 allocat\$3) and threshold | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/12 14:00 |
|----------|----|--|---|------|-----|------------------|
| S19 4 | 17 | (credit\$\$based) and (buffer\$3 near4 allocat\$3) and threshold and robin | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/12 14:06 |
| S19 5 | 2 | (("6104727") or ("6646986")).PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/11/12 14:05 |
| S19 6 | 29 | (credit\$\$based).ti. | US-PGPUB; USPAT; USOCR; EPO; JPO | OR . | ON | 2006/11/12 14:06 |
| S19 7 | 22 | (credit\$\$based).ti. and buffer\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/12 14:06 |
| S19 8 | 9 | (credit\$\$based).ti. and buffer\$3 and threshold | US-PGPUB; USPAT; USOCR; EPO; JPO | OR | ON | 2006/11/12 14:18 |
| S19 9 | 1 | ("6715008").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/11/12 14:18 |

11/12/06 3:22:31 PM C:\Documents and Settings\DNguyen18\My Documents\EAST\Workspaces\09651924.wsp Page 17

Sign in

Google

Web Images Video News Maps more »

credit-based buffer allocation threshold (round Search Preferences

Web Results 1 - 10 of about 46 for credit-based buffer allocation threshold (round-robin) multiprocessor controlle

[PDF] Microsoft PowerPoint - ICPADSnotes.ppt

File Format: PDF/Adobe Acrobat - View as HTML

Memory / Cache Controller. DMBs. Interconnection. Network. Deadlock. Buffers. Network.

End Node. Network ... Idea: decouple the allocation of channel buffer ...

icpads2002.csie.ncu.edu.tw/download/ICPADStutorial_tim_3.pdf - Similar pages

Computer Architecture and VLSI systems Group Publications

Evangelos P. Markatos: **Multiprocessor** Synchronization Primitives with ... Manolis Katevenis: **Buffer** Requirements of **Credit-Based** Flow Control when a Minimum ... archylsi.ics.forth.gr/publications.html - 49k - <u>Cached</u> - <u>Similar pages</u>

[PPT] Interconnection Networks

File Format: Microsoft Powerpoint - View as HTML

Memory / Cache. Controller. Interconnection Network. Network End Node ... Credit-based

link-level flow control; Weighted round-robin fair scheduling of ...

ceng.usc.edu/smart/presentations/archives/AppendixE.ppt - Similar pages

[PDF] Interconnection Networks

File Format: PDF/Adobe Acrobat

Memory / Cache. Controller. Interconnection Network. Network End Node ... Credit-based

(token) flow-control per VC buffer ...

ceng.usc.edu/smart/presentations/archives/AppendixE.pdf - Similar pages

See also: My Computer Architecture Notes from Comps 2004 http ...

cannot assume stall on miss, increasing cache controller complexity (both snoop and ... Ocean would be 2x slower with round robin versus tiled memory ... carlstrom.com/stanford/quals/arch.txt - 259k - Cached - Similar pages

IEEE\slash ACM Transactions on Networking

652--659 Guo-Liang Wu and Jon W. Mark A **buffer allocation** scheme for ATM ... and George Varghese Efficient fair queueing using deficit **round-robin** www.math.utah.edu:8080/ftp/pub/tex/bib/toc/ieeetransnetworking.html - 415k - Cached - Similar pages

[PDF] Fine-Grain Distributed Shared Memory on Clusters of Workstations

File Format: PDF/Adobe Acrobat

Second, it evaluates **buffer allocation** policies for active message implementations. ... the processor **cache controller**. Despite the progress in bus-based ... www.cs.wisc.edu/~schoinas/thesis.single.frm.pdf - <u>Similar pages</u>

IEEE/ACM Transactions on Networking

The variation of optimal bandwidth and **buffer allocation** with the number of ... Comments on "carry-over **round robin**: a simple cell scheduling mechanism for ... wotan.liu.edu/docis/dbl/tranet/index.html - 462k - <u>Cached</u> - <u>Similar pages</u>

@ARTICLE{Schu9603:World, AUTHOR="Henning Schulzrinne", TITLE ...

A general framework for **credit-based** link-by-link flow control is developed by disassociating **buffer** reservaton from credit **allocation**. ... www.cs.columbia.edu/~hqs/bib/net96.bib - 977k - Cached - Similar pages

[PDF] Programming Network Architectures

File Format: PDF/Adobe Acrobat an allocation controller, which manages the computation resources ... The difficulty stems from the fact that network processors use multi-processor ... comet.columbia.edu/~mk/papers/thesis_pna.pdf - Similar pages

Result Page:

1 2 3 4

Next

Try Google Desktop: search your computer as easily as you search the web.

credit-based buffer allocation thresh

Search

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

Google Home - Advertising Programs - Business Solutions - About Google

©2006 Google

Sign in

Google

Web Images Video News Maps more »

credit-based buffer allocation threshold (round Search Preferences

Web Results 11 - 20 of about 46 for credit-based buffer allocation threshold (round-robin) multiprocessor control

[PDF] Cell Broadband Engine Programming Handbook Version 1.0

File Format: PDF/Adobe Acrobat

C.3.2 PPSS L2 Cache Controller - Group 1 (NClk/2) prefetch engine services all active streams simultaneously in a round-robin fashion. Each time a ... www-306.ibm.com/.../techdocs/9F820A5FFA3ECE8C8725716A0062585F/

\$file/BE_Handbook_v1.0_10May2006.pdf - Similar pages

[PDF] SPECIAL: Next Issue:

File Format: PDF/Adobe Acrobat - View as HTML

credit-based flow control (backpressure). A cell in a backpressured service class. (priority

level) can only depart if it. acquires both a buffer-pool ...

acgt.ercim.org/publication/Ercim_News/enw37/EN37.pdf - Similar pages

IPDFI Scalable Flow Control for Interconnection Networks

File Format: PDF/Adobe Acrobat

7.2 Simplified Schematic of a Data Path Scheduler Applying Round-Robin equal to the stop threshold. In general, if the buffer is considered to be ...

e-collection.ethbib.ethz.ch/show?type=diss&nr=15020&part=fulltext - Similar pages

IEEE

Efficient Fair Queueing for ATM Networks Using Uniform Round Robin ... Receiver-Oriented Adaptive Buffer Allocation in Credit-Based Flow Control for ATM ... wotan.liu.edu/docis/dbl/infoco/index.html - 856k - Cached - Similar pages

@INPROCEEDINGS{Roch9803:Cost, AUTHOR="R. Guerin and L. Li and ...

We compare the performance of the existing drop-tail, EB(Equal Buffering) and the proposed FB **buffer** management schemes, with and without RR(Round-Robin) ... www.cs.columbia.edu/~hgs/bib/net99.bib - 977k - Cached - Similar pages

[XLS] Sheet1

File Format: Microsoft Excel

The **controller** design method is based on the work of Z. Li, introducing some ... There are two methods used for **buffer** management, static **threshold buffer** ... www.wseas.org/2004.xls - <u>Similar pages</u>

[PS] A Complete Bibliography of IEEE/ACM Transactions on Networking

File Format: Adobe PostScript

dimensional round-robin sched-. ulers for packet switches with ... A buffer allocation.

scheme for ATM networks: complete sharing based on ...

www.math.utah.edu/pub/tex/bib/ieeetransnetworking.ps.gz - Similar pages

WARNING: Do NOT edit this file. It was produced automatically ...

\NAMECITE{buffer/\penalty0{}bandwidth}{% Presti:1999:STS% }. ... \NAMECITE{cache}{% Braun:1996:PEC% }. \NAMECITE{cache-friendly}{% Rabinovich:2004:DEC% }. ... www.math.utah.edu/pub/tex/bib/ieeetransnetworking.twx - 104k - Cached - Similar pages

[PDF] Designing Efficient Network Interfaces For System Area Networks

File Format: PDF/Adobe Acrobat

Link Controller (LC). TX Buffer. RX Buffer. Bypass FIFO ... round-robin fashion between

them to prevent starvation of one of the paths. Both job sce- ...

deposit.ddb.de/cgi-bin/dokserv?idn=974092428& dok var=d1&dok ext=pdf&filename=974092428.pdf - Similar pages

[PS] Proportional Time Emulation and Simulation of ATM Networks

File Format: Adobe PostScript - View as Text problem with credit-based schemes is that each VC must reserve enough buffer space. to fill the link capacity even though the link is potentially shared by ... hegel.ittc.ku.edu/projects/proteus/docs/SHouse-thesis.ps - Similar pages

> Result Page: Previous 1 2 3 4 **Next**

Try Google Desktop: search your computer as easily as you search the web.

credit-based buffer allocation thresh Search



Search within results | Language Tools | Search Tips

Google Home - Advertising Programs - Business Solutions - About Google

©2006 Google

Sign in

Google

Web Images Video News Maps more »

credit-based buffer allocation threshold (round Search Preferences

Web Results 21 - 30 of about 46 for credit-based buffer allocation threshold (round-robin) multiprocessor control

-*-BibTeX ...

{\bf B.3.2} Hardware, MEMORY STRUCTURES, Design Styles, Cache memories. ... title = "The variation of optimal bandwidth and buffer allocation with the ... www.tug.org/ftp/tex/bib/ieeetransnetworking.bib - 938k - Cached - Similar pages

[PS] Performance Management in ATM Networks

File Format: Adobe PostScript priority trac to preferentially succeed in contention, and round-robin arbitration ... Buffer 150 cells, threshold 35 cells. Buffer 150 cells, no threshold ... www.cl.cam.ac.uk/TechReports/UCAM-CL-TR-393.ps.gz - Similar pages

BigPatents -- Link buffer sharing method and apparatus

H.T. Kung and K. Chang, Receiver-Oriented Adaptive Buffer Allocation in Credit-Based Flow Control for ATM Networks, Proceedings of INFOCOM '95, Apr. 2-6, ... https://www.bigpatents.com/pnum/5781533 - 83k - Cached - Similar pages

From sp at scali.com Fri Nov 1 01:06:45 2002 From: sp at scali.com ...

Buffering - 16KB per port, statically allocated across VLs Flow Control - credit-based, per VL Queuing - Two stage, strict priority/round robin VL ...

www.beowulf.org/archive/2002-November.txt - 872k - Nov 11, 2006 - Cached - Similar pages

10:13 PM UTC, July 12 2006 credits done right, and auto updates of ...
Under WinXP, a single client run on a multiprocessor seems to ping-pong from one
CPU ... Periodically switches from round robin to earliest deadline first. ...
boinc berkeley edu/poll_improved_text.html - 977k - Nov 11, 2006 - Cached - Similar pages

<u>credits done right, and auto updates of BOINC software ...</u> - [<u>Translate this page</u>] Create a BOINC supported work unit **cache** that runs on Linux, so that not every client ... Periodically switches from **round robin** to earliest deadline first. ... boinc.berkeley.edu/poll_improved_text.txt - 702k - <u>Cached</u> - <u>Similar pages</u>

t10 document register 1994 present

... for SCSI-3 Controller Commands Standard T10/94-035R1 Date: 1994/02/04 ... Desc: Tabular data Bulk cable round robin 2.xls T10/02-090R0 Date: 2002/02/27 ... www.t10.org/ftp/t10/dr.txt - 918k - Cached - Similar pages

[Xen-changelog] Merge latest xen-unstable into xen-ia64-unstable ...
-The BVT, Atropos and Round Robin schedulers are part of the normal -Xen ... For trace buffer-enabled builds of Xen these -will insert the event ID and data ...
lists xensource.com/archives/html/xen-changelog/2005-09/msg00467.html - Similar pages

[PS] ATLAS DAQ, EF, LVL2 and DCS

File Format: Adobe PostScript spectrum as a typical SMP multiprocessor, with no overlap at all (and low absolute ... period being the time for the round robin allocation to loop over the ... atlas.web.cern.ch/Atlas/GROUPS/DAQTRIG/TPR/PS_FILES/TPR1-up.bk.ps - Similar pages

Webster's Online Dictionary - The Rosetta Edition

APPARATUS AND METHOD FOR CONTROLLING A SYSTEM BUS OF A **MULTIPROCESSOR** SYSTEM ... APPARATUS AND METHOD FOR INTELLIGENT MULTIPLE-PROBE **CACHE ALLOCATION** ... www.websters-online-dictionary.org/browse/inventions/indexap.html - <u>Similar pages</u>

In order to show you the most relevant results, we have omitted some entries very similar to the 30 already displayed.

If you like, you can repeat the search with the omitted results included.

Result Page: Previous 1 2 3

Try Google Desktop: search your computer as easily as you search the web.

credit-based buffer allocation thresh

Search

Search within results | Language Tools | Search Tips

Google Home - Advertising Programs - Business Solutions - About Google

©2006 Google



Home | Login | Logout | Access Information | Alerts | Sitemap

| | RELEASE 2.1 | | Welcome United State | s Patent and Traden | nark Office | | |
|-------------|--|----------|---|---|----------------------------------|-----------------|-----------|
| Search Res | sults | | BROWSE | SEARCH | IEEE XPLORE GUID | E | SUPPO |
| Your search | "((credit <in>metadata) on matched 7 of 1430374 do not 100 results are displayed</in> | cuments. | | - | er. | ⊠ e-πail | aprinte |
| » Search O | ptions | | | | | | |
| View Sessi | on History | | fy Search | | | | |
| New Searc | <u>h</u> | ((cred | dit <in>metadata) <and> (bit</and></in> | uffer <in>metadata))<ar< td=""><td>nd> (allocate<in>metadata</in></td><td>Search</td><td>3</td></ar<></in> | nd> (allocate <in>metadata</in> | Search | 3 |
| | | □с | heck to search only withir | this results set | | | |
| » Key | | Displ | ay Format: Citatio | n C Citation & Abs | stract | | |
| IEEE JNL | IEEE Journal or Magazine | | | | | | |
| IEE JNL | IEE Journal or Magazine | ← view | v selected items Sele | ect All Deselect All | | | |
| IEEE CNF | IEEE Conference Proceeding | | 1. Receiver-oriented ac | daptive buffer alloca | tion in credit-based flow | control f | or ATM |
| IEE CNF | IEE Conference | * | networks Kung, H.T.; Koling Ch | ana: | | | • |
| IEEE STD | Proceeding IEEE Standard | | INFOCOM '95. Fourte | enth Annual Joint Co | nference of the IEEE Com | puter and | l Commu |
| IEEE 31D | IEEE Standard | | Societies. Bringing Into 2-6 April 1995 Page(s | · · · · · · · · · · · · · · · · · · · | roceedings. IEEE | | |
| | | | Digital Object Identifie | | | | |
| | | | AbstractPlus Full Te Rights and Permissio | | EEE CNF | , | |
| | | | using FIFO buffers Varvarigos, E.A.; Sha Networking, IEEE/AC Volume 5, Issue 5, C Digital Object Identifie | rma, V.; <u>M Transactions on</u> Oct. 1997 Page(s):705 er 10.1109/90.649570 | | nultigigal | bit netwo |
| | | | AbstractPlus Refere Rights and Permissio | | 528 KB) IEEE JNL | | |
| | | | 3. Preventing buffer-cr Chrysos, N.; Kateven High Performance Sw 7-9 June 2006 Page(s Digital Object Identified AbstractPlus Full Te Rights and Permissio | is, M.; <u>vitching and Routing, :</u> s):8 pp. er 10.1109/HPSR.200 xt: <u>PDF(</u> 367 KB) IEI | 6.1709744 | hared ou | tput que |
| · | | | 4. Rate-based flow-cor Yoshigoe, K.; Local Computer Netw 15-17 Nov. 2005 Pag Digital Object Identifie | orks, 2005. 30th Anni e(s):44 - 50 | iversary. The IEEE Confer | ence on | |
| | | | | | | | |

5. Credit determination for flow control in FC-AL Lin Qiang; Xiong Huagang; Zhang Qishan;

Rights and Permissions

AbstractPlus | Full Text: PDF(352 KB) | IEEE CNF

Digital Avionics Systems Conference, 2004. DASC 04. The 23rd

Volume 2, 24-28 Oct. 2004 Page(s):10.A.5 - 10.1-7 Vol.2 AbstractPlus | Full Text: PDF(685 KB) | IEEE CNF Rights and Permissions 6. Probabilistic packet delay guarantee in buffered crossbar switch Duan, Q.; Daigle, J.N.; Performance, Computing, and Communications, 2004 IEEE International Conference on 2004 Page(s):253 - 258 Digital Object Identifier 10.1109/PCCC.2004.1394994 AbstractPlus | Full Text: PDF(714 KB) IEEE CNF Rights and Permissions 7. Resource allocation for quality of service provision in buffered crossbar switches Qiang Duan; Daigle, J.N.; Computer Communications and Networks, 2002. Proceedings. Eleventh International Conference on 14-16 Oct. 2002 Page(s):509 - 513 Digital Object Identifier 10.1109/ICCCN.2002.1043115 AbstractPlus | Full Text: PDF(282 KB) | IEEE CNF Rights and Permissions

indexed by च्चे inspec* Help Contact Us Privacy & Security © Copyright 2006 IEEE - All Rights



Home | Login | Logout | Access Information | Alerts | Sitemap

Welcome United States Patent and Trademark Office

□ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPOF

Results for "((credit<in>metadata) <and> (buffer<in>metadata))<and> (threshold<..." Your search matched 3 of 1430374 documents.

☑e-mail 📇 printer

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

View Session History

New Search

» Key

IEEE Journal or IEEE JNL

Magazine

IEE JNL IEE Journal or Magazine

IEEE Conference IEEE CNF Proceeding

IEE Conference **IEE CNF**

Proceeding

IEEE STD IEEE Standard

Modify Search

(credit<in>metadata) <and> (buffer<in>metadata))<and> (threshold<in>metadat

Search

L Check to search only within this results set

Display Format:

Citation Citation & Abstract

view selected items

Select All Deselect All

1. Preventing buffer-credit accumulations in switches with small, shared output quet

Chrysos, N.; Katevenis, M.;

High Performance Switching and Routing, 2006 Workshop on

7-9 June 2006 Page(s):8 pp.

Digital Object Identifier 10.1109/HPSR.2006.1709744

AbstractPlus | Full Text: PDF(367 KB) | IEEE CNF

Rights and Permissions

2. Rate-based traffic control technique in credit style for ABR service in ATM network

Feng Lihong; Li Zhe; Wang Guangxing;

Communication Technology Proceedings, 1998. ICCT '98. 1998 International Conference

22-24 Oct. 1998 Page(s):441 - 445 vol.1

Digital Object Identifier 10.1109/ICCT.1998.743286

AbstractPlus | Full Text: PDF(256 KB) | IEEE CNF

Rights and Permissions

3. On the effectiveness of buffer sharing in multimedia server network switches with

similar traffic

Yunkai Zhou; Harish Sethu;

Communications, 2002. ICC 2002. IEEE International Conference on

Volume 4, 28 April-2 May 2002 Page(s):2533 - 2536 vol.4

Digital Object Identifier 10.1109/ICC.2002.997299

AbstractPlus | Full Text: PDF(225 KB) | IEEE CNF

Rights and Permissions

Help Contact Us Privacy & Security

© Copyright 2006 IEEE - All Rights



Home | Login | Logout | Access Information | Alerts | Sitemap

Welcome United States Patent and Trademark Office

| □ Search | Results |
|----------|---------|
|----------|---------|

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPOF

Results for "((credit<in>metadata) <and> (buffer<in>metadata))<and> (robin<in&g..." Your search matched 5 of 1430374 documents.

☑ e-mail 🚇 printer

| » Search O | ptions | | | | | |
|---|-------------------------------|--|--|--|--|--|
| <u>View Session History</u> <u>New Search</u> | | Modify Search | | | | |
| | | ((credit <in>metadata) <and> (buffer<in>metadata))<and> (robin<in>metadata) Search ></in></and></in></and></in> | | | | |
| | | Check to search only within this results set | | | | |
| » Key | | Display Format: Citation C Citation & Abstract | | | | |
| IEEE JNL | IEEE Journal or Magazine | _ view selected items Select All Deselect All | | | | |
| IEE JNL | IEE Journal or Magazine | view selected items Select All Deselect All | | | | |
| IEEE CNF | IEEE Conference Proceeding | 1. Preventing buffer-credit accumulations in switches with small, shared output qu Chrysos, N.; Katevenis, M.; | | | | |
| IEE CNF | IEE Conference Proceeding | High Performance Switching and Routing, 2006 Workshop on 7-9 June 2006 Page(s):8 pp. | | | | |
| IEEE STD | IEEE Standard | Digital Object Identifier 10.1109/HPSR.2006.1709744 | | | | |
| | | AbstractPlus Full Text: PDF(367 KB) IEEE CNF Rights and Permissions | | | | |
| | | 2. Client-server performance on flow-controlled ATM networks: a Web database of simulation results Kuang, H.T.; Wang, S.Y.; INFOCOM '97. Sixteenth Annual Joint Conference of the IEEE Computer and Commu Societies. Proceedings IEEE Volume 3, 7-11 April 1997 Page(s):1218 - 1226 vol.3 Digital Object Identifier 10.1109/INFCOM.1997.631147 | | | | |
| | | AbstractPlus Full Text: PDF(768 KB) IEEE CNF Rights and Permissions | | | | |
| | | 3. On the combined input-crosspoint buffered switch with round-robin arbitration Rojas-Cessa, R.; Oki, E.; Chao, H.J.; Communications, IEEE Transactions on Volume 53, Issue 11, Nov. 2005 Page(s):1945 - 1951 Digital Object Identifier 10.1109/TCOMM.2005.858667 | | | | |
| | | AbstractPlus Full Text: PDF(432 KB) IEEE JNL Rights and Permissions | | | | |
| | | 4. A two-dimensional scalable crossbar matrix switch architecture Jong Arm Jum; Sung Hyuk Byun; Byung Jun Ahn; Seung Yeob Nam; Dan Keun Sung Communications, 2003. ICC '03. IEEE International Conference on Volume 3, 11-15 May 2003 Page(s):1892 - 1896 vol.3 Digital Object Identifier 10.1109/ICC.2003.1203927 | | | | |
| | | AbstractPlus Full Text: PDF(414 KB) IEEE CNF Rights and Permissions | | | | |
| | | 5. Congestion avoidance with BUC (buffer utilization control) gateways and RFCN | | | | |

feedback congestion notification)

Performance, Computing, and Communications Conference, 1997. IPCCC 1997., IEEE

Ziegler, T.; Clausen, H.D.;

<u>International</u>

5-7 Feb. 1997 Page(s):410 - 418 Digital Object Identifier 10.1109/PCCC.1997.581545 AbstractPlus | Full Text: PDF(956 KB) IEEE CNF

Rights and Permissions

Indexed by inspec* Help Contact Us Privacy & Security © Copyright 2006 IEEE - All Rights



Subscribe (Full Service) Register (Limited Service, Free) Login

Search: The ACM Digital Library
The Guide

+credit +buffer +threshold +robin

SEARCH

THE ACK DIGITAL LIBRARY

Feedback Report a problem Satisfaction survey

Terms used <u>credit</u> <u>buffer</u> <u>threshold</u> <u>robin</u>

Found **56** of **189**,**785**

Sort results by

relevance

Save results to a Binder ? Search Tips

Try an Advanced Search Try this search in The ACM Guide

Display expanded form results

☐ Open results in a new window

Results 1 - 20 of 56

Result page: $1 \quad 2 \quad 3$

Relevance scale

Reliable and efficient hop-by-hop flow control

Cüneyt Özveren, Robert Simcoe, George Varghese

 ∇

October 1994 ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Communications architectures, protocols and applications SIGCOMM '94, Volume 24 Issue 4

Publisher: ACM Press

Full text available: pdf(1.03 MB)

Additional Information: full citation, abstract, references, citings, index terms

Hop-by-hop flow control can be used to fairly share the bandwidth of a network among competing flows. No data is lost even in overload conditions; yet each flow gets access to the maximum throughput when the network is lightly loaded. However, some schemes for hop-by-hop flow control require too much memory; some of them are not resilient to errors. We propose a scheme for making hop-by-hop flow control resilient and show that it has advantages over schemes proposed by Kung. We also describ ...

2 Xunet 2: lessons from an early wide-area ATM testbed

Charles R. Kalmanek, Srinivasan Keshav, William T. Marshall, Samuel P. Morgan, Robert C. Restrick

February 1997 IEEE/ACM Transactions on Networking (TON), Volume 5 Issue 1

Publisher: IEEE Press

Full text available: pdf(231.69 KB) Additional Information: full citation, references, index terms

Keywords: asynchronous transfer mode, available bit rate, constant bit rate, variable bit rate

3 Dynamics of random early detection

Dong Lin, Robert Morris

October 1997 ACM SIGCOMM Computer Communication Review, Proceedings of the ACM SIGCOMM '97 conference on Applications, technologies, architectures, and protocols for computer communication SIGCOMM

'97, Volume 27 Issue 4

Publisher: ACM Press

Full text available: pdf(1.50 MB)

Additional Information: full citation, abstract, references, citings, index <u>terms</u>

In this paper we evaluate the effectiveness of Random Early Detection (RED) over traffic types categorized as non-adaptive, fragile and robust, according to their responses to congestion. We point out that RED allows unfair bandwidth sharing when a mixture of the three traffic types shares a link. This unfairness is caused by the fact that at any given time RED imposes the same loss rate on all flows, regardless of their bandwidths. We propose Fair Random Early Drop (FRED), a modified version of ...

4 Dynamics of TCP traffic over ATM networks

Allyn Romanow, Sally Floyd

October 1994 ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Communications architectures, protocols and applications SIGCOMM '94, Volume 24 Issue 4

Publisher: ACM Press

Full text available: pdf(1.12 MB)

Additional Information: full citation, abstract, references, citings, index <u>terms</u>

We investigate the performance of TCP connections over ATM networks without ATM-level congestion control, and compare it to the performance of TCP over packet-based networks. For simulations of congested networks, the effective throughput of TCP over ATM can be quite low when cells are dropped at the congested ATM switch. The low throughput is due to wasted bandwidth as the congested link transmits cells from "corrupted" packets, i.e., packets in which at least one cell is dropp ...

A control theoretical approach to congestion control in packet networks Dirceu Cavendish, Mario Gerla, Saverio Mascolo October 2004 IEEE/ACM Transactions on Networking (TON), Volume 12 Issue 5

Publisher: IEEE Press

Full text available: pdf(708.31 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we introduce a control theoretical analysis of the closed-loop congestion control problem in packet networks. The control theoretical approach is used in a proportional rate controller, where packets are admitted into the network in accordance with network buffer occupancy. A Smith Predictor is used to deal with large propagation delays, common to high speed backbone networks. The analytical approach leads to accurate predictions regarding both transients as well as steady-stat ...

Keywords: packet networks, quality of service (QoS), stability analysis, transient analysis

6 Multicasting protocols for high-speed, wormhole-routing local area networks

Mario Gerla, Prasasth Palnati, Simon Walton

August 1996 ACM SIGCOMM Computer Communication Review , Conference proceedings on Applications, technologies, architectures, and protocols for computer communications SIGCOMM '96, Volume 26 Issue 4

Publisher: ACM Press

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> Full text available: pdf(110.64 KB)

Wormhole routing LANs are emerging as an effective solution for high-bandwidth, lowlatency interconnects in distributed computing and cluster computing applications. An important example is the 640 Mb/s crossbar-based Myrinet. A key property of conventional LANs, which is valuable for many distributed applications, is transparent, reliable network-level multicast. It is desirable to retain this property also in wormhole LANs. Unfortunately, efficient, reliable multicasting in wormhole LANs is p ...

A time-slotted-CDMA architecture and adaptive resource allocation method for

connections with diverse QoS guarantees

Samar Singh, Satish K. Tripathi

September 2003 Wireless Networks, Volume 9 Issue 5

Publisher: Kluwer Academic Publishers

Full text available: pdf(249.70 KB) Additional Information: full citation, abstract, references, index terms

We consider a time-slotted W-CDMA system for mobile stations which are connected to the wired internet. We first present an architecture for such a system that is based on a request-permission protocol incorporating power control for "Best Effort" transmissions on the uplink. The requesting mobiles are permitted to transmit in the next time slot with a specified power according to a schedule computed by the Base Station. To devise this scheduling method, we formulate a globally optimizing intege ...

Keywords: QoS guarantees, S-CDMA, bandwidth-sharing, transmission scheduling, wireless MAC

Design, analysis, and implementation of DVSR: a fair high-performance protocol for packet rings



Violeta Gambiroza, Ping Yuan, Laura Balzano, Yonghe Liu, Steve Sheafor, Edward Knightly February 2004 IEEE/ACM Transactions on Networking (TON), Volume 12 Issue 1

Publisher: IEEE Press

Full text available: pdf(671.81 KB)

Additional Information: full citation, abstract, references, citings, index

The Resilient Packet Ring (RPR) IEEE 802.17 standard is a new technology for high-speed backbone metropolitan area networks. A key performance objective of RPR is to simultaneously achieve high utilization, spatial reuse, and fairness, an objective not achieved by current technologies such as SONET and Gigabit Ethernet nor by legacy ring technologies such as FDDI. The core technical challenge for RPR is the design of a bandwidth allocation algorithm that dynamically achieves these three properti ...

Keywords: dynamic bandwidth allocation, fairness, metro rings, scheduling

9 Aggregate nearest neighbor queries in spatial databases



Publisher: ACM Press

Additional Information: full citation, abstract, references, index terms Full text available: pdf(3.84 MB)

Given two spatial datasets P (e.g., facilities) and Q (queries), an aggregate nearest neighbor (ANN) query retrieves the point(s) of P with the smallest aggregate distance(s) to points in Q. Assuming, for example, n users at locations q1,...qn, an ANN query outputs the facility $p \in P$ that minimizes the *sum* of distances | pqi| for $1 \le i \le ...$

Keywords: Spatial database, aggregation, nearest neighbor queries

10 Scalable and fault-tolerant support for variable bit-rate data in the exedra streaming



server

Stergios V. Anastasiadis, Kenneth C. Sevcik, Michael Stumm November 2005 ACM Transactions on Storage (TOS), Volume 1 Issue 4

Publisher: ACM Press

Additional Information: full citation, abstract, references, index terms Full text available: pdf(1.01 MB)

We describe the design and implementation of the Exedra continuous media server, and experimentally evaluate alternative resource management policies using a prototype system that we built. Exedra has been designed to provide scalable and efficient support for variable bit-rate media streams whose compression efficiency leads to reduced storage space and bandwidth requirements in comparison to constant bit-rate streams of equivalent quality. We examine alternative disk striping policies, and qua ...

Keywords: Content distribution, multimedia compression

11 VirtualClock: a new traffic control algorithm for packet-switched networks

Lixia Zhang

May 1991 ACM Transactions on Computer Systems (TOCS), Volume 9 Issue 2

Publisher: ACM Press

Full text available: pdf(1.76 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

One of the challenging research issues in building high-speed packet-switched networks is how to control the transmission rate of statistical data flows. This paper describes a new traffic control algorithm, VirtualClock, for high-speed network applications. VirtualClock monitors the average transmission rate of statistical data flows and provides every flow with guaranteed throughput and low queueing delay. It provides firewall protection among individual flows, as in a TD ...

Keywords: data traffic control, performance guarantee, rate-based flow-control algorithms, statistical multiplexing, time-division-multiplexing

12 Understanding and improving TCP performance over networks with minimum rate guarantees



Wu-chang Feng, Dilip D. Kandlur, Debanjan Saha, Kang G. Shin

April 1999 IEEE/ACM Transactions on Networking (TON), Volume 7 Issue 2

Publisher: IEEE Press

Full text available: pdf(258.07 KB) Additional Information: full citation, references, citings, index terms

Keywords: TCP, differentiated services, integrated services, queue management

13 A quality of service architecture

Andrew Campbell, Geoff Coulson, David Hutchison

April 1994 ACM SIGCOMM Computer Communication Review, Volume 24 Issue 2

Publisher: ACM Press

Full text available: pdf(2.22 MB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>citings</u>, <u>index terms</u>

For applications relying on the transfer of multimedia, and in particular continuous media, it is essential that quality of service (QoS) is guaranteed system-wide, including endsystems, communications systems and networks. Although researchers have addressed many isolated areas of QoS provision, little attention has so far been paid to the definition of an integrated and coherent framework that incorporates QoS interfaces, management and mechanisms across all architectural layers. To address t ...

14 Scalable distributed visualization using off-the-shelf components

Alan Heirich, Laurent Moll

October 1999 Proceedings of the 1999 IEEE symposium on Parallel visualization and

graphics

Publisher: ACM Press

Full text available: pdf(1.81 MB)

Additional Information: full citation, abstract, references, citings, index

This paper describes a visualization architecture for scalable computer systems. The architecture is currently being prototyped for use in Beowulf-class clustered systems. A set of OpenGL frame buffers are driven in parallel by a set of CPUs. The visualization architecture merges the contents of these frame buffers by user-programmable associative and commulative combining operations. The system hardware is built from off-the-shelf components including OpenGL accelerators, Field Programmabl ...

Keywords: Beowulf, FPGA, OpenGL, cluster, fat-tree, gigabit, visualization

15 Network Protocols

Andrew S. Tanenbaum

December 1981 ACM Computing Surveys (CSUR), Volume 13 Issue 4

Publisher: ACM Press

Full text available: pdf(3.37 MB) Additional Information: full citation, references, citings, index terms

16 A parallel embedded-processor architecture for ATM reassembly

Richard F. Hobson, P. S. Wong

February 1999 IEEE/ACM Transactions on Networking (TON), Volume 7 Issue 1

Publisher: IEEE Press

Full text available: pdf(331.21 KB) Additional Information: full citation, references, citings, index terms

Keywords: ATM, embedded systems, medium access control, segmentation and reassembly

17 Constructing collaborative desktop storage caches for large scientific datasets

Sudharshan S. Vazhkudai, Xiaosong Ma, Vincent W. Freeh, Jonathan W. Strickland, Nandan Tammineedi, Tyler Simon, Stephen L. Scott

August 2006 ACM Transactions on Storage (TOS), Volume 2 Issue 3

Publisher: ACM Press

Full text available: pdf(833.76 KB) Additional Information: full citation, abstract, references, index terms

High-end computing is suffering a data deluge from experiments, simulations, and apparatus that creates overwhelming application dataset sizes. This has led to the proliferation of high-end mass storage systems, storage area clusters, and data centers. These storage facilities offer a large range of choices in terms of capacity and access rate, as well as strong data availability and consistency support. However, for most end-users, the "last mile" in their analysis pipeline o ...

Keywords: Distributed storage, parallel I/O, scientific data management, serverless storage system, storage cache, storage networking, storage resoucce management, storage scavenging, striped storage

18 A QoS adaptive transport system: design, implementation and experience Andrew Campbell, Geoff Coulson

February 1997 Proceedings of the fourth ACM international conference on Multimedia



Publisher: ACM Press

Full text available: pdf(1.29 MB)

Additional Information: full citation, references, citings, index terms

19 Link and channel measurement: A simple mechanism for capturing and replaying



wireless channels

Glenn Judd, Peter Steenkiste

August 2005 Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05

Publisher: ACM Press

Full text available: pdf(6.06 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An important challenge in physical emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we examine the possibility of using on-card signal strength measurements to capture wireless channel traces. A key advantage of this approach is the simplicity and ubiquity with which these measurements can be obtained since virtually all wireless devices provide the req ...

Keywords: channel capture, emulation, wireless

20 Endpoint admission control: architectural issues and performance



Lee Breslau, Edward W. Knightly, Scott Shenker, Ion Stoica, Hui Zhang August 2000 ACM SIGCOMM Computer Communication Review , Proceedings of the conference on Applications, Technologies, Architectures, and Protocols for Computer Communication SIGCOMM '00, Volume 30 Issue 4

Publisher: ACM Press

Full text available: pdf(231.84 KB)

Additional Information: full citation, abstract, references, citings, index terms

The traditional approach to implementing admission control, as exemplified by the Integrated Services proposal in the IETF, uses a signalling protocol to establish reservations at all routers along the path. While providing excellent quality-of-service, this approach has limited scalability because it requires routers to keep per-flow state and to process per-flow reservation messages. In an attempt to implement admission control without these scalability problems, several recent papers hav ...

Results 1 - 20 of 56

Result page: 1 2 3

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc. Terms of Usage Privacy Policy Code of Ethics Contact Us

Useful downloads: Adobe Acrobat QuickTime Windows Media Player Real Player



Subscribe (Full Service) Register (Limited Service, Free) Login

The ACM Digital Library
O The Guide

+credit +buffer +threshold +robin

SEARCH



Feedback Report a problem Satisfaction survey

Terms used <u>credit buffer threshold robin</u>

Found **56** of **189**,**785**

Sort results by

relevance

? Search Tips •

Try an Advanced Search Try this search in The ACM Guide

'Display results

expanded form

Open results in a new window

Save results to a Binder

Relevance scale

Results 21 - 40 of 56

21 The BlackWidow High-Radix Clos Network

Steve Scott, Dennis Abts, John Kim, William J. Dally

May 2006 ACM SIGARCH Computer Architecture News, Proceedings of the 33rd annual international symposium on Computer Architecture ISCA '06, Volume

Result page: previous $1 \ 2 \ 3$

Publisher: IEEE Computer Society, ACM Press

Full text available: pdf(815.14 KB) Additional Information: full citation, abstract, index terms

This paper describes the radix-64 folded-Clos network of the Cray BlackWidow scalable vector multiprocessor. We describe the BlackWidow network which scales to 32K processors with a worstcase diameter of seven hops, and the underlying high-radix router microarchitecture and its implementation. By using a high-radix router with many narrow channels we are able to take advantage of the higher pin density and faster signaling rates available in modern ASIC technology. The BlackWidow router is an 80 ...

²² Virtual clock: a new traffic control algorithm for packet switching networks





💪 L. Zhang

August 1990 ACM SIGCOMM Computer Communication Review, Proceedings of the ACM symposium on Communications architectures & protocols SIGCOMM '90, Volume 20 Issue 4

Publisher: ACM Press

Full text available: pdf(1.19 MB)

Additional Information: full citation, abstract, references, citings, index terms

A challenging research issue in high speed networking is how to control the transmission rate of statistical data flows. This paper describes a new algorithm, VirtualClock, for data traffic control in high-speed networks. VirtualClock maintains the statistical multiplexing flexibility of pocket switching while ensuring each data flow its reserved average throughput rate at the same time. The algorithm has been tested through simulation.

23 Scheduling computations on a software-based router

Xiaohu Qie, Andy Bavier, Larry Peterson, Scott Karlin

June 2001 ACM SIGMETRICS Performance Evaluation Review, Proceedings of the 2001 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '01, Volume 29 Issue 1

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings Full text available: pdf(1.46 MB)

Recent efforts to add new services to the Internet have increased the interest in software-

based routers that are easy to extend and evolve. This paper describes our experiences implementing a software-based router, with a particular focus on the main difficulty we encountered: how to schedule the router's CPU cycles. The scheduling decision is complicated by the desire to differentiate the level of service for different packet flows, which leads to two fundamental conflicts: (1) assigning proce ...

²⁴ Flexible cross-domain event delivery for quality-managed multimedia applications

Christian Poellabauer, Karsten Schwan

August 2005 ACM Transactions on Multimedia Computing, Communications, and Applications (TOMCCAP), Volume 1 Issue 3

Publisher: ACM Press

Full text available: pdf(389.96 KB) Additional Information: full citation, abstract, references, index terms

To meet end users' quality-of-service (QoS) requirements, online quality management for multimedia applications must include appropriate allocation of the underlying computing platform's resources. Previous work has developed novel operating system (OS) functionality for dynamic QoS management, including multimedia or real-time CPU schedulers and OS extensions for online performance monitoring and for adaptations, as well as QoS-aware applications that adapt their behavior to gain additional ben ...

Keywords: Event delivery, dynamic code generation, operating system, quality management, quality-of-service, real-time events

25 Query processing for relational data: Supporting ad-hoc ranking aggregates



Chengkai Li, Kevin Chen-Chuan Chang, Ihab F. Ilyas June 2006 Proceedings of the 2006 ACM SIGMOD international conference on Management of data SIGMOD '06

Publisher: ACM Press

Full text available: pdf(344.23 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a principled framework for efficient processing of ad-hoc top-k (ranking) aggregate queries, which provide the k groups with the highest aggregates as results. Essential support of such queries is lacking in current systems, which process the queries in a naïve materialize-group-sort scheme that can be prohibitively inefficient. Our framework is based on three fundamental principles. The Upper-Bound Principle dictates the requirements of early pruning, and ...

Keywords: OLAP, aggregate query, decision support, ranking, top-k query processing

26 Dynamic data <u>fusion for future sensor networks</u>



Umakishore Ramachandran, Rajnish Kumar, Matthew Wolenetz, Brian Cooper, Bikash Agarwalla, Junsuk Shin, Phillip Hutto, Arnab Paul

August 2006 ACM Transactions on Sensor Networks (TOSN), Volume 2 Issue 3

Publisher: ACM Press

Full text available: pdf(2.44 MB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

DFuse is an architectural framework for dynamic application-specified data fusion in sensor networks. It bridges an important abstraction gap for developing advanced fusion applications that takes into account the dynamic nature of applications and sensor networks. Elements of the DFuse architecture include a fusion API, a distributed role assignment algorithm that dynamically adapts the placement of the application task graph on the network, and an abstraction migration facility that aids such ...

Keywords: Sensor network, data fusion, energy awareness, in-network aggregation, middleware, platform, role assignment

27 Speedy wireless: Improving TCP performance over wireless networks with

collaborative multi-homed mobile hosts

Kyu-Han Kim, Kang G. Shin

June 2005 Proceedings of the 3rd international conference on Mobile systems, applications, and services MobiSys '05

Publisher: ACM Press

Additional Information: full citation, abstract, references Full text available: pdf(1.01 MB)

Multi-homed mobile hosts situated in physical proximity may spontaneously team up to run high-bandwidth applications by pooling their low wireless wide-area network (WWAN) bandwidths together for communication with a remote application server and utilizing their high-bandwidth wireless local-area network (WLAN) in ad-hoc mode for aggregation and distribution of application contents among the participating mobile hosts. In this paper, we first describe the need for such a mobile collaborative com ...

28 Run-time adaptation in river



Remzi H. Arpaci-Dusseau

February 2003 ACM Transactions on Computer Systems (TOCS), Volume 21 Issue 1

Publisher: ACM Press

Full text available: pdf(849.04 KB) Additional Information: full citation, abstract, references, index terms

We present the design, implementation, and evaluation of run-time adaptation within the River dataflow programming environment. The goal of the River system is to provide adaptive mechanisms that allow database query-processing applications to cope with performance variations that are common in cluster platforms. We describe the system and its basic mechanisms, and carefully evaluate those mechanisms and their effectiveness. In our analysis, we answer four previously unanswered and important que ...

Keywords: Performance availability, clusters, parallel I/O, performance faults, robust performance, run-time adaptation

²⁹ Idletime scheduling with preemption intervals





Lars Eggert, Joseph D. Touch

October 2005 ACM SIGOPS Operating Systems Review , Proceedings of the twentieth ACM symposium on Operating systems principles SOSP '05, Volume 39 Issue

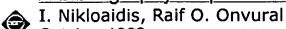
Publisher: ACM Press

Full text available: pdf(2.02 MB) Additional Information: full citation, abstract, references, index terms

This paper presents the idletime scheduler; a generic, kernel-level mechanism for using idle resource capacity in the background without slowing down concurrent foreground use. Many operating systems fail to support transparent background use and concurrent foreground performance can decrease by 50% or more. The idletime scheduler minimizes this interference by partially relaxing the work conservation principle during preemption intervals, during which it serves no background requests eve ...

Keywords: background processing, disk scheduler, idletime scheduling, network scheduler, preemption interval, queuing, resource scheduler

30 A bibliography on performance issues ATM networks



October 1992 ACM SIGCOMM Computer Communication Review, Volume 22 Issue 5

Publisher: ACM Press

Full text available: pdf(1.37 MB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>citings</u>, <u>index terms</u>

The Asynchronous Transfer Mode (ATM) is the transport mode of choice for B-ISDN. In order for high speed networks to become a reality, a number of performance issues has to be resolved. In recent years, there has been a growing interest in the literature in developing performance models to explore a wide range of performance problems varying from understanding the performance of a switch architecture to implementing efficient congestion control mechanisms and light weight transport protocols. In ...

31 A declarative approach to optimize bulk loading into databases

Sihem Amer-Yahia, Sophie Cluet

June 2004 ACM Transactions on Database Systems (TODS), Volume 29 Issue 2

Publisher: ACM Press

Full text available: pdf(1.00 MB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Applications, such as warehouse maintenance, need to load large data volumes regularly. The efficiency of loading depends on the resources that are available at the source and at the target systems. Our work aims to understand the performance criteria that are involved in bulk loading data into a database and to devise tailored optimization strategies. Unlike commercial systems and previous research on the same topic, our approach follows the fundamental database principle of physical-logical ind ...

Keywords: Declarative bulk loading, algebra, recovery, side-effects

32 An explicit rate ABR algorithm for new‐ generation ATM switches Iftekhar Hussain, Kuldip Bains

September 1999 International Journal of Network Management, Volume 9 Issue 5

Publisher: John Wiley & Sons, Inc.

Full text available: pdf(841.30 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we present a new explicit rate ABR switch algorithm which takes advantage of the per‐ VC queueing and scheduling mechanisms of next‐ generation ATM switches to obtain an efficient algorithm of low complexity. The performance characteristics of the proposed algorithm such as fairness, link utilization, and queue control are illustrated via simulations. Copyright © 2000 John Wiley & Sons, Ltd.

33 Query evaluation techniques for large databases

Goetz Graefe

June 1993 ACM Computing Surveys (CSUR), Volume 25 Issue 2

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(9.37 MB) terms, review

Database management systems will continue to manage large data volumes. Thus, efficient algorithms for accessing and manipulating large sets and sequences will be required to provide acceptable performance. The advent of object-oriented and extensible database systems will not solve this problem. On the contrary, modern data models exacerbate the problem: In order to manipulate large sets of complex objects as efficiently as today's database systems manipulate simple records, query-processi ...

Keywords: complex query evaluation plans, dynamic query evaluation plans, extensible database systems, iterators, object-oriented database systems, operator model of parallelization, parallel algorithms, relational database systems, set-matching algorithms, sort-hash duality

34 A SMART scheduler for multimedia applications

Jason Nieh, Monica S. Lam

May 2003 ACM Transactions on Computer Systems (TOCS), Volume 21 Issue 2

Publisher: ACM Press

Full text available: pdf(570.87 KB) Additional Information: full citation; abstract, references, index terms

Real-time applications such as multimedia audio and video are increasingly populating the workstation desktop. To support the execution of these applications in conjunction with traditional non-real-time applications, we have created SMART, a Scheduler for Multimedia And Real-Time applications. SMART supports applications with time constraints, and provides dynamic feedback to applications to allow them to adapt to the current load. In addition, the support for real-time applications is integrat ...

Keywords: Scheduling, multimedia, proportional sharing, real-time

The state of the art in locally distributed Web-server systems

Valeria Cardellini, Emiliano Casalicchio, Michele Colajanni, Philip S. Yu June 2002 ACM Computing Surveys (CSUR), Volume 34 Issue 2

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.41 MB) terms

The overall increase in traffic on the World Wide Web is augmenting user-perceived response times from popular Web sites, especially in conjunction with special events. System platforms that do not replicate information content cannot provide the needed scalability to handle large traffic volumes and to match rapid and dramatic changes in the number of clients. The need to improve the performance of Web-based services has produced a variety of novel content delivery architectures. This article w ...

Keywords: Client/server, World Wide Web, cluster-based architectures, dispatching algorithms, distributed systems, load balancing, routing mechanisms

36 Hoard: a scalable memory allocator for multithreaded applications

Emery D. Berger, Kathryn S. McKinley, Robert D. Blumofe, Paul R. Wilson November 2000 ACM SIGARCH Computer Architecture News, ACM SIGOPS Operating Systems Review , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX, Volume 28, 34 Issue 5, 5

Publisher: ACM Press

Full text available: pdf(430.66 KB)

Additional Information: full citation, abstract, references, citings, index <u>terms</u>

Parallel, multithreaded C and C++ programs such as web servers, database managers, news servers, and scientific applications are becoming increasingly prevalent. For these applications, the memory allocator is often a bottleneck that severely limits program performance and scalability on multiprocessor systems. Previous allocators suffer from problems that include poor performance and scalability, and heap organizations that introduce false sharing. Worse, many allocators exhibit a dramatic incr ...

37 Hoard: a scalable memory allocator for multithreaded applications

Emery D. Berger, Kathryn S. McKinley, Robert D. Blumofe, Paul R. Wilson November 2000 ACM SIGPLAN Notices, Volume 35 Issue 11

Publisher: ACM Press

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u> Full text available: pdf(1.47 MB)

Parallel, multithreaded C and C++ programs such as web servers, database managers, news servers, and scientific applications are becoming increasingly prevalent. For these applications, the memory allocator is often a bottleneck that severely limits program performance and scalability on multiprocessor systems. Previous allocators suffer from problems that include poor performance and scalability, and heap organizations that introduce false sharing. Worse, many allocators exhibit a dramatic incr ...

38 A formal model for reasoning about adaptive QoS-enabled middleware



Nalini Venkatasubramanian, Carolyn Talcott, Gul A. Agha

January 2004 ACM Transactions on Software Engineering and Methodology (TOSEM), Volume 13 Issue 1

Publisher: ACM Press

Full text available: pdf(1.42 MB) Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Systems that provide distributed multimedia services are subject to constant evolution; customizable middleware is required to effectively manage this change. Middleware services for resource management execute concurrently with each other, and with application activities, and can, therefore, potentially interfere with each other. To ensure cost-effective QoS in distributed multimedia systems, safe composability of resource management services is essential. In this article, we present a meta-arc ...

Keywords: Middleware services, actors, meta-object models, multimedia, quality-ofservice, reflection, theoretical foundations

39 The design, implementation and evaluation of SMART: a scheduler for multimedia





applications

Jason Nieh, Monica S. Lam

October 1997 ACM SIGOPS Operating Systems Review, Proceedings of the sixteenth ACM symposium on Operating systems principles SOSP '97, Volume 31 Issue

Publisher: ACM Press

Full text available: pdf(2.48 MB) Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

⁴⁰ VEX: a volume exploratorium: an integrated toolkit for interactive volume visualization





Larry Gelberg, David Kamins, Jeff Vroom

May 1989 Proceedings of the 1989 Chapel Hill workshop on Volume visualization **VVS '89**

Publisher: ACM Press

Full text available: pdf(2.35 MB) Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

Keywords: surface tiling, toolkit, visualization, volume rendering

Results 21 - 40 of 56 Result page: previous 1 2 3 next

> The ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc. Terms of Usage Privacy Policy Code of Ethics Contact Us

> Useful downloads: Adobe Acrobat QuickTime Windows Media Player Real Player



Subscribe (Full Service) Register (Limited Service, Free) Login

Search: The ACM Digital Library O The Guide

+credit +buffer +threshold +robin

SEARCH



Feedback Report a problem Satisfaction survey

Terms used <u>credit</u> <u>buffer</u> <u>threshold</u> <u>robin</u>

Found **56** of **189,785**

Sort results by

Display

• relevance

Save results to a Binder ? Search Tips

Try an Advanced Search Try this search in The ACM Guide

▼ expanded form Open results in a new resuits window

Results 41 - 56 of 56

Result page: previous 1 2 3

Relevance scale

41 Architecture and systems: Teleport messaging for distributed stream programs

William Thies, Michal Karczmarek, Janis Sermulins, Rodric Rabbah, Saman Amarasinghe June 2005 Proceedings of the tenth ACM SIGPLAN symposium on Principles and practice of parallel programming

Publisher: ACM Press

Full text available: pdf(352.12 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we develop a new language construct to address one of the pitfalls of parallel programming: precise handling of events across parallel components. The construct, termed teleport messaging, uses data dependences between components to provide a common notion of time in a parallel system. Our work is done in the context of the Synchronous Dataflow (SDF) model, in which computation is expressed as a graph of independent components (or actors) that communicate in regular ...

Keywords: StreamIt, dependence analysis, digital signal processing, embedded, event handling, synchronous dataflow

42 A study of source-level compiler algorithms for automatic construction of pre-

execution code

Dongkeun Kim, Donald Yeung

August 2004 ACM Transactions on Computer Systems (TOCS), Volume 22 Issue 3

Publisher: ACM Press

Full text available: pdf(1.55 MB)

Additional Information: full citation, abstract, references, index terms

Pre-execution is a promising latency tolerance technique that uses one or more helper threads running in spare hardware contexts ahead of the main computation to trigger long-latency memory operations early, hence absorbing their latency on behalf of the main computation. This article investigates several source-to-source C compilers for extracting pre-execution thread code automatically, thus relieving the programmer or hardware from this onerous task. We present an aggressive profile-driven co ...

Keywords: Data prefetching, memory-level parallelism, multithreading, pre-execution, prefetch conversion, program slicing, speculative loop parallelization

43 Improving instruction cache performance in OLTP Stavros Harizopoulos, Anastassia Ailamaki





September 2006 ACM Transactions on Database Systems (TODS), Volume 31 Issue 3

Publisher: ACM Press

Full text available: pdf(783.16 KB) Additional Information: full citation, abstract, references, index terms

Instruction-cache misses account for up to 40% of execution time in online transaction processing (OLTP) database workloads. In contrast to data cache misses, instruction misses cannot be overlapped with out-of-order execution. Chip design limitations do not allow increases in the size or associativity of instruction caches that would help reduce misses. On the contrary, the effective instruction cache size is expected to further decrease with the adoption of multicore and multithreading ...

Keywords: Instruction cache, cache misses

44 Astrolabe: A robust and scalable technology for distributed system monitoring,



management, and data mining

Robbert Van Renesse, Kenneth P. Birman, Werner Vogels

May 2003 ACM Transactions on Computer Systems (TOCS), Volume 21 Issue 2

Publisher: ACM Press

Full text available: pdf(341.62 KB)

Additional Information: full citation, abstract, references, citings, index terms

Scalable management and self-organizational capabilities are emerging as central requirements for a generation of large-scale, highly dynamic, distributed applications. We have developed an entirely new distributed information management system called Astrolabe. Astrolabe collects large-scale system state, permitting rapid updates and providing on-the-fly attribute aggregation. This latter capability permits an application to locate a resource, and also offers a scalable way to track sys ...

Keywords: Aggregation, epidemic protocols, failure detection, gossip, membership, publish-subscribe, scalability

45 Size-based scheduling to improve web performance



Mor Harchol-Balter, Bianca Schroeder, Nikhil Bansal, Mukesh Agrawal

May 2003 ACM Transactions on Computer Systems (TOCS), Volume 21 Issue 2

Publisher: ACM Press

Full text available: pdf(486.07 KB)

Additional Information: full citation, abstract, references, citings, index

Is it possible to reduce the expected response time of every request at a web server, simply by changing the order in which we schedule the requests? That is the question we ask in this paper. This paper proposes a method for improving the performance of web servers servicing static HTTP requests. The idea is to give preference to requests for small files or requests with short remaining file size, in accordance with the SRPT (Shortest Remaining Processing Time) scheduling policy. The imple ...

Keywords: Conservation law, SJF, SRPT, networking, scheduling, system performance and design, web servers

Mesh parameterization: Painting detail



Nathan A. Carr, John C. Hart

August 2004 ACM Transactions on Graphics (TOG), Volume 23 Issue 3

Publisher: ACM Press

Full text available: pdf(25.68 MB) Additional Information: full citation, abstract, references

mov(25:32 MIN)

Surface painting is a technique that allows a user to paint a texture directly onto a surface, usually with a texture atlas: a 1:1 mapping between the surface and its texture image. Many good automatic texture atlas generation methods exist that evenly distribute texture samples across a surface based on its area and/or curvature, and some are even. sensitive to the frequency spectrum of the input texture. However, during the surface painting process, the texture can change non-uniformly and unpr ...

Keywords: 3D painting, Mesh parametrization, face clustering, texture atlas

47 ECOSystem: managing energy as a first class operating system resource

Heng Zeng, Carla S. Ellis, Alvin R. Lebeck, Amin Vahdat

October 2002 ACM SIGPLAN Notices, ACM SIGARCH Computer Architecture News, ACM SIGOPS Operating Systems Review , Proceedings of the 10th international conference on Architectural support for programming languages and operating systems ASPLOS-X, Volume 37, 30, 36 Issue 10, 5, 5

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings Full text available: pdf(1.17 MB)

Energy consumption has recently been widely recognized as a major challenge of computer systems design. This paper explores how to support energy as a first-class operating system resource. Energy, because of its global system nature, presents challenges beyond those of conventional resource management. To meet these challenges we propose the Currentcy Model that unifies energy accounting over diverse hardware components and enables fair allocation of available energy among applications. Our par ...

48 BASE: Using abstraction to improve fault tolerance

Miguel Castro, Rodrigo Rodrigues, Barbara Liskov

August 2003 ACM Transactions on Computer Systems (TOCS), Volume 21 Issue 3

Publisher: ACM Press

Full text available: pdf(438.18 KB) Additional Information: full citation, abstract, references, index terms

Software errors are a major cause of outages and they are increasingly exploited in malicious attacks. Byzantine fault tolerance allows replicated systems to mask some software errors but it is expensive to deploy. This paper describes a replication technique, BASE, which uses abstraction to reduce the cost of Byzantine fault tolerance and to improve its ability to mask software errors. BASE reduces cost because it enables reuse of off-the-shelf service implementations. It improves availability ...

Keywords: Byzantine fault tolerance, N-version programming, asynchronous systems, proactive recovery, state machine replication

49 Embedded application design using a real-time OS

David Stepner, Nagarajan Rajan, David Hui

June 1999 Proceedings of the 36th ACM/IEEE conference on Design automation DAC '99

Publisher: ACM Press

Full text available: pdf(105.02 KB) Additional Information: full citation, references, citings, index terms

50 Iterative spatial join Edwin H. Jacox, Hanan Samet



September 2003 ACM Transactions on Database Systems (TODS), Volume 28 Issue 3

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(314.43 KB)

The key issue in performing spatial joins is finding the pairs of intersecting rectangles. For unindexed data sets, this is usually resolved by partitioning the data and then performing a plane sweep on the individual partitions. The resulting join can be viewed as a two-step process where the partition corresponds to a hash-based join while the plane-sweep corresponds to a sort-merge join. In this article, we look at extending the idea of the sortmerge join for one-dimensional data to multiple ...

Keywords: Spatial join, external memory algorithms, plane-sweep, spatial databases

On the use of multiple hops in next generation wireless systems

B. S. Manoj, K. J. Kumar, Christo Frank D., C. Siva Ram Murthy

March 2006 Wireless Networks, Volume 12 Issue 2

Publisher: Kluwer Academic Publishers

Full text available: pdf(1.73 MB) Additional Information: full citation, abstract, references, index terms

Traditional cellular networks provide a centralized wireless networking paradigm within the wireless domain with the help of fixed infrastructure nodes such as Base Stations (BSs). On the other hand, Ad hoc wireless networks provide a fully distributed wireless networking scheme with no dependency on fixed infrastructure nodes. Recent studies show that the use of multihop wireless relaying in the presence of infrastructure based nodes improves system capacity of wireless networks. In this paper, ...

Keywords: analytical modeling, hybrid wireless network, integrated cellular and ad hoc relaying, multihop cellular, next generation wireless networks, performance analysis

52 Session 2: The top speed of flash worms



Stuart Staniford, David Moore, Vern Paxson, Nicholas Weaver

October 2004 Proceedings of the 2004 ACM workshop on Rapid malcode

Publisher: ACM Press

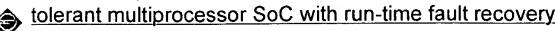
Full text available: pdf(365.68 KB) Additional Information: full citation, abstract, references, index terms

Flash worms follow a precomputed spread tree using prior knowledge of all systems vulnerable to the worm's exploit. In previous work we suggested that a flash worm could saturate one million vulnerable hosts on the Internet in under 30 seconds[18]. We grossly over-estimated.

In this paper, we revisit the problem in the context of single packet UDP worms (inspired by Slammer and Witty). Simulating a flash version of Slammer, calibrated by current Internet latency measurements and observ ...

Keywords: flash worm, modeling, simulation, worms

53 Session 4: processor and communication centric SOC design: Prototyping a fault-



Xinping Zhu, Wei Qin

July 2006 Proceedings of the 43rd annual conference on Design automation DAC '06

Publisher: ACM Press

Full text available: pdf(578.65 KB) Additional Information: full citation, abstract, references, index terms

Modern integrated circuits (ICs) are becoming increasingly complex. The complexity makes it difficult to design, manufacture and integrate these high performance ICs. The advent of multiprocessor Systems-on-chips (SoCs) makes it even more challenging for programmers to utilize the full potential of the computation resources on the chips. In the mean time, the complexity of the chip design creates new reliability challenges. As a result, chip designers and users cannot fully exploit the tremendou ...

Keywords: fault-tolerance, multiprocessor system, network-on-chip, retargetable simulation, run-time verification, system-on-chip

54 Contention in balancing networks resolved (extended abstract)

Leonidas Hadjimitsis, Marios Mavronicolas

June 1998 Proceedings of the seventeenth annual ACM symposium on Principles of distributed computing

Publisher: ACM Press

Full text available: pdf(1.02 MB)

Additional Information: full citation, references, index terms

55 Cluster I/O with River: making the fast case common

Remzi H. Arpaci-Dusseau, Eric Anderson, Noah Treuhaft, David E. Culler, Joseph M. Hellerstein, David Patterson, Kathy Yelick

May 1999 Proceedings of the sixth workshop on I/O in parallel and distributed systems

Publisher: ACM Press

Full text available: pdf(1.20 MB) Additional Information: full citation, references, citings, index terms

56 Improving storage system availability with D-GRAID

Muthian Sivathanu, Vijayan Prabhakaran, Andrea C. Arpaci-Dusseau, Remzi H. Arpaci-Dusseau

May 2005 ACM Transactions on Storage (TOS), Volume 1 Issue 2

Publisher: ACM Press

Full text available: pdf(700.30 KB) Additional Information: full citation, abstract, references, index terms

We present the design, implementation, and evaluation of D-GRAID, a gracefully degrading and quickly recovering RAID storage array. D-GRAID ensures that most files within the file system remain available even when an unexpectedly high number of faults occur. D-GRAID achieves high availability through aggressive replication of semantically critical data, and fault-isolated placement of logically related data. D-GRAID also recovers from failures quickly, restoring only live file system data to a h ...

Keywords: Block-based storage, Disk array, RAID, fault isolation, file systems, smart disks

Results 41 - 56 of 56 Result page: previous $\frac{1}{2}$ 3

> The ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc. Terms of Usage Privacy Policy Code of Ethics Contact Us

> Useful downloads: Adobe Acrobat QuickTime Windows Media Player Real Player